

Performance Analysis of Telecommunication System with Feedforward Control Mechanism

Z. Navickas, A. Žvironienė

Department of Applied Mathematics, Kaunas University of Technology,

Studentų str. 50-323, LT-51368 Kaunas, Lithuania, e-mails: zenonas.navickas@ktu.lt, ausra.zvironiene@ktu.lt

R. Rindzevičius, P. Tervydis

Department of Telecommunications, Kaunas University of Technology,

Studentų str. 50-423, LT-51368 Kaunas, Lithuania, e-mails: ramutis.rindzevicius@ktu.lt, paulius.tervydis@ktu.lt

Introduction

The goal of this paper is to analyze processes in an Internet node queueing system with local feedforward control. The proposed analysis method based on Moore and Mealy automata is much simpler and universal than Markov processes applied previously by authors to the analysis of such queueing systems [1, 2, 3]. Feedback and feedforward control is typically used in the telecommunication systems which have time varying transmission parameters. A rich literature exists in the area of data packets transmission process control systems. The majority of them analyze the feedback or feedforward control mechanism to regulate variable data packets transmission parameters, to avoid congestion and overdone delay, and to achieve high data transmission link utilization. The new type of the control system over the Internet allows remote monitoring and control of process plants using for control systems feedback and feedforward actions [8]. The feedforward and backward multirate control mechanism for IP transport network is applied to avoid data link congestion and data packet losses [11]. Two stages bit rate control mechanism must achieve that the originating part sends data at the maximum possible rate to the link [11]. Control algorithms to dynamically adjust bit rate of sender are design to guarantee stable performance measures of data transmission network based on continuous or discrete time feedback [9]. A two-level hierarchy is used for the Internet based control systems with two compensators located at the feedback channel that results the multirate control scheme efficiently reduce the effect of Internet time delay [7, 8]. One possible way to solve problem of network congestion and guarantee the quality of service is to use the feedback control mechanism to adapt the sender output bit rate depending on the transmission link state [6, 10]. Implementation of Moore and Mealy automata in of one channel queueing system

simulation allows investigation of processes in a complex system such as telecommunication network [5].

The main goal of the paper is to develop an efficient feedforward control method in data transmission network. This type of control is best deployed in control systems design applications where behavior of the controlled variable is well understood and can be measured at the sender site.

The paper is organized as follows. First section introduces some new approaches to feedback and feedforward mechanisms. Internet adaptive sender bit rate control system architecture is presented in second section. The simulation model based on Moore and Mealy automata are described in third. The simulation results are presented and discussed in fourth section. In last section, we present our conclusions.

Internet node architectures with multirate control mechanisms

The IP based network feedback control scheme that uses a feedback channel to relay measurements, taken at the receiver shown on Fig.1. The feedback channel of Internet often is not suitable for real time data transmission, because cannot guarantee availability and stability of end-to-end delay. We propose the feedforward control mechanism without these disadvantages.

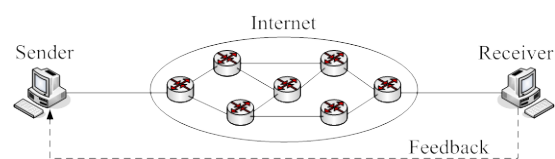


Fig. 1. Multirate feedback control scheme in the Internet network

Feedforward control system architecture used to compute the sender output bitrate, matching for the level of

Assume that the “0-th” component (data source) contains some data, the „1st“ component (the sender) generates the data packets (with three data sending rate adjustment options: 1) to increase the rate, 2) keep the same rate, 3) to decrease the data sending rate) and the „2nd“ system component (the receiver) is used to service the packets. The infinite amount of packets wait in the “0-th” component, and the packets served in the „1st“ and „2nd“ components (data transmission channel and receiver) are returned to “0-th” component. If a packet arrives to the „1st“ component and finds it busy (because the capacity M of buffer is finite), it will be lost. In the „1st“ component the packets are served according data transmission rate and are transited to the „2nd“ component. If the buffer of the „2nd“ component is filled till buffer threshold R_1 , then sender increases the data packet transmission rate. If the buffer of the „2nd“ component is filled between the thresholds R_1 and R_2 , when the sender keeps the same data sending rate. If the buffer in the „2nd“ component approximates to overflow, and the packets are filled from the threshold R_2 to N (N – the amount of states in the buffer of the „2nd“ component), then this device sends a signal to „0-th“ component to decrease the data sending rate.

In the investigated system we have an infinite number of data packets all time. Thus, we create a close service system. It unifies the creation of surjections that describe the operation of such system.

$(\xi_n^{(0)}, n \in Z_0)$ – the interarrival times between packets of the sender output flow, $(\xi_n^{(1)}, n \in Z_0)$ – the data transmission rates, $(\eta_n, n \in Z_0)$ – the data servicing times in the „2nd“ channel, $(\bar{\eta}_n, n \in Z_0)$ – the controllable servicing times.

The standard variables $S_n^{(k)}, t_n$ are used for the creation of surjections [4]. The logical unitary Heaviside function is used for description of surjections

$$\mathbf{1}(t) = \begin{cases} 0, & t < 0, \\ 1, & t \geq 0. \end{cases} \quad (2)$$

In the bellow conditions meets the symbol Inf , it means $+\infty$.

We introduce the variables for surjections. Let's suppose that $n \in N$. Then:

- t_n – „timer“ – ($t_0 = 0, 0 < t_1 < t_2 < \dots$) - it defines the time, when any event occurs

$$t_n = \min(S_n^{(0)}; S_n^{(1)}; S_n^{(2)}). \quad (3)$$

- $S_{n+1}^{(k)}, k = 0, 1, 2$ – the controlled variables, defining the time moment t_{n+1} , when the value t_n is known, $S_{n+1}^{(k)}$ can be equal:

a) $S_n^{(k)} - (0 < S_n^{(k)} < +\infty)$ shows that the „k-th“ component will finish to serve the packets;

b) $t_n + \bar{\eta}_i, i = 1, 2$ – the ordinary packet is served;

c) Inf – the “k-th” component is free.

$$S_{n+1}^{(0)} = S_n^{(0)} + \xi_{t_0}^{(0)} \cdot \mathbf{1}(t_n - S_n^{(0)}), \quad (4)$$

$$S_{n+1}^{(1)} = S_n^{(1)} \cdot \left((1 - \mathbf{1}(t_n - S_n^{(1)})) \cdot (1 - \mathbf{1}(S_n^{(1)} - Inf)) + \mathbf{1}(S_n^{(1)} - Inf) \cdot \mathbf{1}(-z_n^{(1)}) + (t_n + \bar{\eta}_1) \cdot \mathbf{1}(t_n - S_n^{(1)}) \cdot \mathbf{1}(z_n^{(1)} - 1) + \mathbf{1}(S_n^{(1)} - Inf) \cdot \mathbf{1}(z_n^{(1)} - 1) + Inf \cdot \mathbf{1}(t_n - S_n^{(1)}) \cdot \mathbf{1}(-z_n^{(1)}) \right), \quad (5)$$

$$S_{n+1}^{(2)} = S_n^{(2)} \cdot \left((1 - \mathbf{1}(t_n - S_n^{(2)})) \cdot (1 - \mathbf{1}(S_n^{(2)} - Inf)) + \mathbf{1}(S_n^{(2)} - Inf) \cdot \mathbf{1}(-z_n^{(2)}) + (t_n + \xi_{t_2}^{(1)}) \cdot \mathbf{1}(t_n - S_n^{(2)}) \cdot \mathbf{1}(z_n^{(2)} - 1) + \mathbf{1}(S_n^{(2)} - Inf) \cdot \mathbf{1}(z_n^{(2)} - 1) + Inf \cdot \mathbf{1}(t_n - S_n^{(2)}) \cdot \mathbf{1}(-z_n^{(2)}) \right), \quad (6)$$

here $\pm\alpha + Inf = Inf, Inf - Inf = 0$, when $|\alpha| < +\infty$.

- $v_n^{(i)}, i = 1, 2$ – the varieties of changes in i -th channel:

$$\begin{cases} v_n^{(1)} = \mathbf{1}(t_n - S_n^{(0)}) - \mathbf{1}(t_n - S_n^{(1)}), \\ v_n^{(2)} = \mathbf{1}(t_n - S_n^{(1)}) - \mathbf{1}(t_n - S_n^{(2)}). \end{cases} \quad (7)$$

- $z_n^{(i)}, i = 0, 1, 2$ – the number of packets in i -th buffer:

$$z_{n+1}^{(0)} = Inf, \quad (8)$$

$$z_{n+1}^{(1)} = z_n^{(1)} + \mathbf{1}(M - z_n^{(1)} - \mathbf{1}(t_n - S_n^{(0)})) + \mathbf{1}(t_n - S_n^{(1)}) \cdot v_n^{(1)}, \quad (9)$$

$$z_{n+1}^{(2)} = z_n^{(2)} + \mathbf{1}(N - z_n^{(2)} - \mathbf{1}(t_n - S_n^{(1)})) + \mathbf{1}(t_n - S_n^{(2)}) \cdot v_n^{(2)}. \quad (10)$$

- $l_i, i = 0, 1, 2$ - the numbers of packets:

$$l_0 = l_0 + \mathbf{1}(S_n^{(0)} - t_n), \quad (11)$$

$$l_1 = l_1 + \mathbf{1}(S_n^{(1)} - t_n), \quad (12)$$

$$l_2 = l_2 + \mathbf{1}(S_n^{(2)} - t_n). \quad (13)$$

The feedback is described by variable $\bar{\eta}_n$

$$\begin{aligned} \bar{\eta}_n = & \eta_n \left(k_1 \cdot \mathbf{1}(R_1 - z_n^{(2)}) + \right. \\ & + k_2 \cdot \mathbf{1}(z_n^{(2)} - R_1 - 1) \cdot \mathbf{1}(R_2 - z_n^{(2)}) + \\ & \left. + k_3 \cdot \mathbf{1}(z_n^{(2)} - R_2 - 1) \right). \end{aligned} \quad (14)$$

- $N > R_2 > R_1 > 0, R_1, R_2 = 1, 2, \dots, N-1$ – the critical values for regimes, $k_1, k_2, k_3 \geq 0$ – the increase (decrease) coefficients of transmission rate.

The surjections will be described using the recursion formulas in such order f_r, f_l, g_l, g_r .

$$f_r(w_n) = x_n, x_n = (t_n; v_n^{(1)}, v_n^{(2)}), x_n \in X, \quad (15)$$

$$f_l(x_n, z_{n-1}) = y_n, \quad (16)$$

$$y_n = (t_n; v_n^{(1)}, v_n^{(2)}; z_n^{(1)}, z_n^{(2)}), y_n \in Y, \quad (17)$$

$$g_l(x_n, z_{n-1}) = z_n, z_n = (z_n^{(1)}, z_n^{(2)}), z_n \in Z, \quad (18)$$

$$g_r(w_n, y_n) = w_{n+1}, w_n = (S_n^{(0)}, S_n^{(1)}, S_n^{(2)}), w_n \in W. \quad (19)$$

Let the initial states of automata system are:

$$l_0 = 0, l_1 = 0, l_2 = 0, \quad (20)$$

$$S_0^{(0)} = 0, S_0^{(1)} = Inf, S_0^{(2)} = Inf, \quad (21)$$

$$z_0^{(1)} = 0, z_0^{(2)} = 0, n = 0. \quad (22)$$

$$\left(\xi_1^{(0)}, Inf, Inf \right) = w_1. \quad (23)$$

All characteristics (for instance, the probability defining how long 1 packet (2 packets, 3 packets, etc.) is in the system, the mean queue size; the mean waiting time in the buffer, etc.) may be calculated from the implementation using values $(n, t_n, z_n), n=0,1,2,\dots$, that are calculated by formulas from imitation model (3–23).

Simulation results

The proposed data sending rate control mechanism was modeled in the Mathcad's environment. Its algorithm is given by

$$\begin{aligned}
& l_0 \leftarrow 0; \quad l_1 \leftarrow 0; \quad l_2 \leftarrow 0; \quad n \leftarrow 0; \\
& S_n^{(0)} \leftarrow 0; \quad S_n^{(1)} \leftarrow Inf; \quad S_n^{(2)} \leftarrow Inf; \\
& z_n^{(1)} \leftarrow 0; \\
& \text{while } n \leq n_max \\
& \quad t_n \leftarrow \min(S_n^{(0)}, S_n^{(1)}, S_n^{(2)}); \\
& \quad l_0 \leftarrow l_0 + \mathbf{1}(S_n^{(0)} - t_n); \\
& \quad l_1 \leftarrow l_1 + \mathbf{1}(S_n^{(1)} - t_n); \\
& \quad l_2 \leftarrow l_2 + \mathbf{1}(S_n^{(2)} - t_n); \\
& \quad \eta_i \leftarrow \eta^* \cdot [k_1 \cdot \mathbf{1}(R_1 - z_n^{(2)}) + \\
& \quad + k_2 \cdot \mathbf{1}(z_n^{(2)} - R_1 - 1) \cdot \\
& \quad \cdot \mathbf{1}(R_2 - z_n^{(2)}) + k_3 \cdot \mathbf{1}(z_n^{(2)} - R_2 - 1)]; \\
& \quad \xi_{l_0}^{(0)} \leftarrow \eta_i; \\
& \quad z_{n+1}^{(1)} \leftarrow z_n^{(1)} + \mathbf{1}(M - z_n^{(1)} - \mathbf{1}(t_n - S_n^{(0)})) + \\
& \quad + \mathbf{1}(t_n - S_n^{(1)}) \cdot [\mathbf{1}(t_n - S_n^{(0)}) - \mathbf{1}(t_n - S_n^{(1)})]; \\
& \quad z_{n+1}^{(2)} \leftarrow z_n^{(2)} + \mathbf{1}(M - z_n^{(2)} - \mathbf{1}(t_n - S_n^{(1)})) + \\
& \quad + \mathbf{1}(t_n - S_n^{(2)}) \cdot [\mathbf{1}(t_n - S_n^{(1)}) - \mathbf{1}(t_n - S_n^{(2)})]; \quad (24) \\
& \quad S_{n+1}^{(0)} \leftarrow S_n^{(0)} + \xi_{l_0}^{(0)} \cdot \mathbf{1}(t_n - S_n^{(0)}); \\
& \quad S_{n+1}^{(1)} \leftarrow S_n^{(1)} \cdot [(1 - \mathbf{1}(t_n - S_n^{(1)})) \cdot \\
& \quad \cdot (1 - \mathbf{1}(S_n^{(1)} - Inf)) + \mathbf{1}(S_n^{(1)} - Inf) \cdot \\
& \quad \cdot \mathbf{1}(-z_n^{(1)})] + (t_n + \eta_i) \cdot [\mathbf{1}(t_n - S_n^{(1)}) \cdot \\
& \quad \cdot \mathbf{1}(z_n^{(1)} - 1) + \mathbf{1}(S_n^{(1)} - Inf) \cdot \mathbf{1}(z_n^{(1)} - 1)] + \\
& \quad + Inf \cdot \mathbf{1}(t_n - S_n^{(1)}) \cdot \mathbf{1}(-z_n^{(1)}); \\
& \quad S_{n+1}^{(2)} \leftarrow S_n^{(2)} \cdot [(1 - \mathbf{1}(t_n - S_n^{(2)})) \cdot \\
& \quad \cdot (1 - \mathbf{1}(S_n^{(2)} - Inf)) + \mathbf{1}(S_n^{(2)} - Inf) \cdot \\
& \quad \cdot \mathbf{1}(-z_n^{(2)})] + (t_n + \xi_{l_2}^{(1)}) \cdot [\mathbf{1}(t_n - S_n^{(2)}) \cdot \\
& \quad \cdot \mathbf{1}(z_n^{(2)} - 1) + \mathbf{1}(S_n^{(2)} - Inf) \cdot \mathbf{1}(z_n^{(2)} - 1)] + \\
& \quad + Inf \cdot \mathbf{1}(t_n - S_n^{(2)}) \cdot \mathbf{1}(-z_n^{(2)}); \\
& \quad n \leftarrow n + 1; \\
& \text{Rez.: } \{t\}, \{\eta\}, \{\xi^{(0)}\}, \{z^{(1)}\}, \{z^{(2)}\}.
\end{aligned}$$

The data sending rate is selected by multiplying the particular value of η^* by k_1, k_2 or k_3 coefficients, which are selected by the buffer occupancy level $z_n^{(2)}$ according to the buffer thresholds R_1 and R_2 .

Two cases of data sending rate control were modeled:

1) The value η^* is constant (for example, it is equal to the mean value of data transmission channel rate). The simulation results are shown in Fig. 7 and Fig. 9;

2) The value η^* is equal to the η_{i-1} (the previous data sending rate is used to select the current data sending rate) The simulation results are shown in Fig. 8, Fig. 10 and Fig. 11.

To evaluate how the feedforward mechanism selects the data sending rate when the data transmission rate in the channel changes, we used the following data channel rate model

$$R(t) := \begin{cases} 256 \text{ kbps}, & \text{if } t < t_1, \\ 512 \text{ kbps}, & \text{if } t_1 \leq t < t_2, \\ 256 \text{ kbps}, & \text{if } t_2 \leq t < t_3, \\ 512 \text{ kbps}, & \text{if } t_3 \leq t < t_4, \\ 256 \text{ kbps}, & \text{if } t_4 \leq t. \end{cases} \quad (25)$$

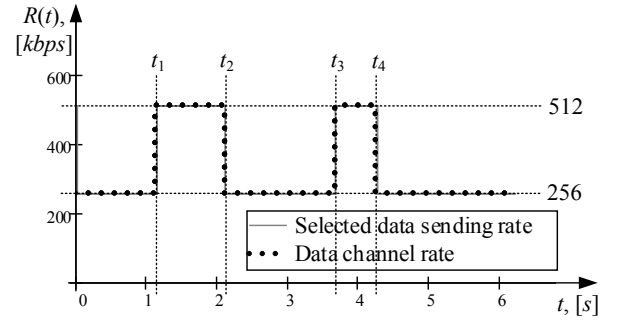


Fig. 7. The simulation results in the first case of feedforward control mechanism (when $\eta^*=1/512$ kbps, $R_1=5$, $R_2=10$, $N=15$, $M=10$, $k_1=0.5$, $k_2=1$, $k_3=2$)

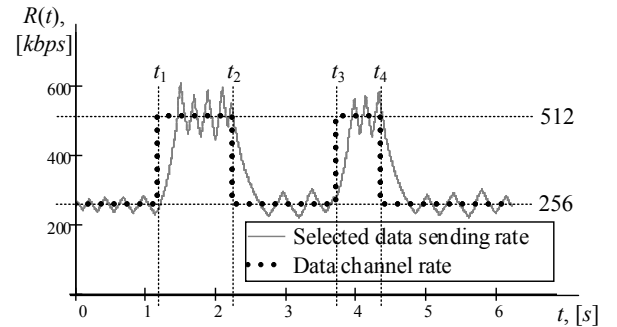


Fig. 8. The simulation results in the second case of feedforward control mechanism (when $R_1=1$, $R_2=2$, $N=3$, $M=10$, $k_1=0.99$, $k_2=1$, $k_3=1.01$)

It is obvious, that the closer the selected data sending rate to the data transmission rate of the channel – the better efficiency of the channel's throughput utilization.

In the first case the efficiency was achieved much better (the selected data sending rate matches for the data channel bitrate), because the channel transmission rate

changes only to in the fixed 256 kbps and 512 kbps boundaries, which are ideal for the selected η^* , k_1 , k_2 and k_3 values.

The Fig. 9 shows what happens when the 1st case scenario is used, but the selected value $\eta^*=1/384$ kbps with the coefficients k_1 , k_2 and k_3 doesn't match the values 256 kbps and 512 kbps.

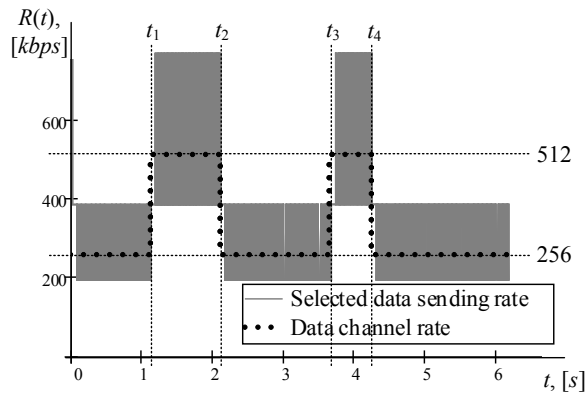


Fig. 9. The simulation results in the first case of feedback control mechanism ($\eta^*=1/384$ kbps, $R_1=5$, $R_2=10$, $N=15$, $M=10$, $k_1=0.5$, $k_2=1$, $k_3=2$)

Therefore, in a general case, the adaptive (second) data sending rate control mechanism may prove better.

It is illustrated in the Fig. 10 and Fig. 11 how the selected data sending rate depends on the different values of k_1 , k_2 , k_3 , R_1 , R_2 and N .

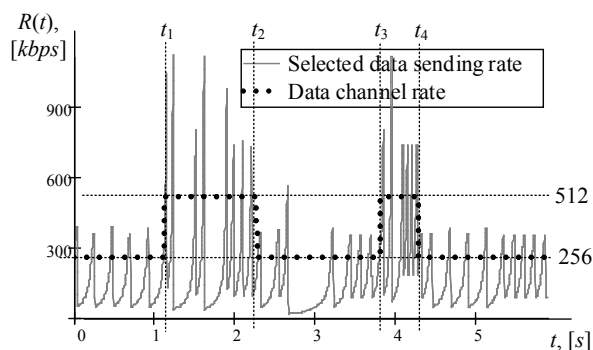


Fig. 10. The simulation results in the second case of feedback control mechanism ($R_1=2$, $R_2=4$, $N=6$, $M=10$, $k_1=0.95$, $k_2=1$, $k_3=2$)

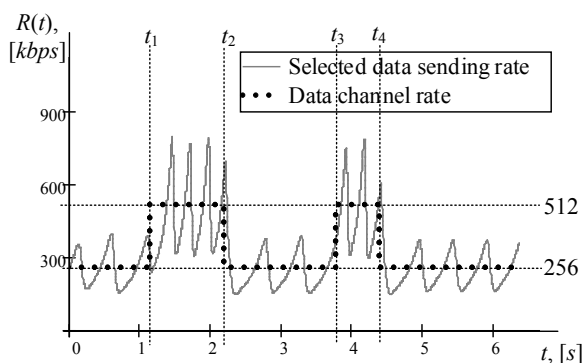


Fig. 11. The simulation results in the second case of feedback control mechanism ($R_1=4$, $R_2=6$, $N=8$, $M=10$, $k_1=0.99$, $k_2=1$, $k_3=1.05$)

As can be seen the selection method of optimal data sending rate control values could be chosen as the task for future works, because inappropriate selection of the values decreases the data channel rate usage efficiency significantly.

Conclusion and future work

In this paper, we have made an attempt to explore control theory in designing feedforward adaptive sender output bitrate local control mechanism based on output buffer occupancy level. We argued that the proposed bitrate control mechanism is applicable to Internet technologies - the realization is simpler, data packet transmission is safer in comparison with the classical feedback output bitrate control scheme. We derive local control algorithm too dynamically or determine adjust output bitrate of sender. The system simulation model is implemented using the convolution of Moore and Mealy automata.

The output buffer threshold feedforward control mechanism may be used in the practical real-time data transmission systems to ensure the non-critical packet transmission delay and to optimize the data transmission channel bandwidth usage.

The key component of the proposed feedforward sender output bit rate control mechanism is the way to compute the sender transmission bit rate matching with level of output buffer occupancy.

The focus of our future work is to investigate the two levels feedforward/feedbackward output bit rate control mechanism for multirate control scheme which efficiently reduces the effect of Internet network variable delay for data packets transmission performance measures.

References

1. **Rindzevičius R., Tervydis P., Žvirionienė A., Navickas Z.** Feedback Impact Analysis of Queueing System with Stable States //Proc. 2009 IEEE Fourth International Multi-Conference on Computing in the Global Information Technology (ICCGI.2009). – Cannes, August 2009. – P. 63–68.
2. **Rindzevičius R., Adlys G.** Performance Measures of Data Network Node with Buffer Threshold Control // Electronics and Electrical Engineering. – Kaunas: Technologija, 2007. – No. 8(80). – P. 29–34.
3. **Rindzevičius R., Tervydis P.** Performance Analysis of an Unreliable Queueing System with buffer Threshold Control and a Reserved Channel // Electronics and Electrical Engineering. – Kaunas: Technologija, 2009. – No. 2(90). – P. 15–20.
4. **Tervydis P.** Research of Control and Management of Data Transmission over Internet Network. // Electronics and Electrical Engineering.– Kaunas: Technologija, 2006. – No. 5(69). – P. 57–62.
5. **Žvirionienė A., Navickas Z., Rindzevičius R.** The expression of the Telecommunication system with an infinite queue by the convolution of Moore and Mealy automata // Proceedings of the 27th International Conference on Information Technology Interfaces. – Croatia, 2005. – P. 669–672.
6. **Lu X., Morando R. O., El Zarki M.** Understanding Video Quality and its use in Feedback Control // The VIP Lab, Dept. of ICS, UC Irvine, CA 92697, 2002. – P. 1–12.

7. **Bolot J. C., Turletti T.** A rate control mechanism for packet video in the Internet // Proceedings of IEEE INFOCOM '94. – Toronto, Canada, June 1994.
8. **Yang L., Yang S. H.** Multirate Control in Internet-Based Control Systems // IEEE Transactions on Systems, Man, and Cybernetics, 2007. – Part C. – No. 37(2). – P. 185–192.
9. **Hossain M., Harsha M., H., Sirisena R.** Adaptive Resource Management in Mobile Wireless Networks Using Feedback Control Theory // Telecommunication Systems, Kluwer Academic Publishers, 2004. – P. 401–415.
10. **Floardh O., Johansson K. H., Johansson M.** A New Feedback Control Mechanism for Error Correction in Packet-Switched Networks // Proceedings of the 44th IEEE Conference on Decision and Control, and the European Control Conference. – Seville, Spain, December 2005. – P. 488–493.
11. **Xiong N., He Y., Cao J., Hom H., Lin C.** An Efficient Flow Control Algorithm for Multi-rate Multicast Networks // IPOM 2004: 2004 IEEE Workshop on IP Operations and Management proceedings: self-measurement & self-management of IP networks & services. – Beijing, China, 2004. – P. 74–81.

Received 2010 04 17

Z. Navickas, A. Žvirionienė, R. Rindzevičius, P. Tervydis. Performance Analysis of telecommunication System with Feedback Control Mechanism // Electronics and Electrical Engineering. – Kaunas: Technologija, 2010. – No. 9(105). – P. 3–8.

The proposed feedforward adaptive bitrate control method has advantages over feedback control scheme. Advantages of the method were demonstrated by means of simulation. Delayed feedback signal from long paths in IP network causes excessive queue growth and data packet loss. Adaptive local feedforward sender output bit rate control mechanism removes IP network delay impact to data packets transmission performance measures. The key component of the proposed feedforward sender output bit rate control mechanism is the way to compute the sender transmission bit rate matching with level of output buffer occupancy. Investigated bit rate control mechanism is based on generating feedback signal when the queue length in the output buffer exceeds the defined occupancy thresholds. The bit rate controller works according to a feedback signal, adaptively reducing or increasing sender output bit rate and keeps the system within an acceptable range of performance avoiding transmission link of congestion. Simulation model of proposed feedforward bit rate control mechanism based on Moore and Mealy automata is simple and universe. We carried out simulations to verify our proposed feedforward bit rate control mechanism and evaluate an effectiveness of our solution. Ill. 11, bibl. 11 (in English; abstracts in English and Lithuanian).

Z. Navickas, A. Žvirionienė, R. Rindzevičius, P. Tervydis. Telekomunikacijų sistemos, valdomos grįžtamojo ryšio mechanizmo, efektyvumo analizė // Elektronika ir elektrotechnika. – Kaunas: Technologija, 2010. – Nr. 9(105). – P. 3–8.

Pasiūlytas tiesioginis adaptyvus duomenų perdavimo spartos valdymo metodas turi pranašumų, palyginti su grįžtamojo ryšio valdymo schema. Šie pranašumai atsispindi modeliavimo rezultatuose. Dėl grįžtamojo ryšio signalo vėlinimo ilgose interneto tinklo linijose atsiranda nemažų perdavimo buferiuose svyravimų ir duomenų paketų nuostolių. Adaptyvus vietinis tiesioginis duomenų perdavimo spartos valdymo mechanizmas pašalina IP tinklo vėlinimo įtaką duomenų paketų perdavimo kokybės rodikliams. Pagrindinė pasiūlyto adaptyvaus vietinio tiesioginio duomenų perdavimo spartos valdymo mechanizmo idėja yra duomenų perdavimo spartos parinkimas atsižvelgiant į duomenų perdavimo buferio užpildymo lygį. Tiriamojo siųstuvo duomenų perdavimo spartos valdymo mechanizmo veikimas pagrįstas grįžtamoju signalu, gaunamu iš duomenų siuntimo buferio, ir jo reikšmė priklauso nuo buferio užpildymo slenksčių. Duomenų paketų perdavimo spartos regulatorius didina arba mažina bitų perdavimo spartą priklausomai nuo grįžtamojo signalo reikšmės ir kartu palaiko pageidautiną duomenų paketų perdavimo tinklu kokybę ir išvengia perdavimo grandies perkrovos. Imitacinis mechanizmo modelis remiasi Muro ir Milio automatais ir yra universalus ir palyginti paprastas. Modeliavimo metu gauti rezultatai patvirtino mūsų priimtų sprendimų efektyvumą. Il. 11, bibl. 11 (anglų kalba; santraukos anglų ir lietuvių k.).

DOI: 10.5755/j02.eie.9156