Controller Performance Assessment of a Photovoltaic Generator Terminated in a Current-Mode-Buck-Convertor-Load

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Abstract—In this paper, a comprehensive control analysis is presented, with emphasis on regulating the terminal voltage of a photovoltaic generator, interfaced with a current mode controlled buck DC-DC converter, which is, in turn terminated by a battery. The combined generator-convertor-load system possesses nonlinear behaviour, and is subject to environmental conditions, thus causing the control task complicated. Additionally, analyses of small signal equations reveal a possible unstable condition. Hence, a new method of designing a controller for worst-case scenario is presented. Additionally, results of implementing a typical linear controller (PI), which can be designed only in reference to a single nominal operation point, revealing a varied responses. Simulation and results of mathematical analysis are presented to verify the proposed method.

Index Terms—Control design; DC-DC power converters; Mathematical analysis; Photovoltaic systems.

I. INTRODUCTION

Photovoltaic generator (PVG) based systems are mainly designed to extract the maximum attainable energy from their solar source [1], utilizing diverse of maximum power point tracking (MPPT) algorithms [2], [3]. Nonetheless, following recent developments in microgrids concept [4], [5], renewable energy generators are now required to operate below the maximum power point (MPP). In addition, they need to match generation and consumption levels in isolated microgrids [6]-[8]. As a result, the supervising power management controller (PMC) should be able to operate in both modes. PMC-generated output may serve as PVG voltage or current reference in addition to the interfacing converter duty cycle. Quantitative research [9]-[14] has indicated that controlling PVG voltage is the best option due to higher stability and lower dependence on solar irradiation.

However, operation below MPP may require wide PVG voltage variation as opposed to the MPPT mode, where PVG voltage range is relatively narrow [15]. Recently, it was shown that source properties significantly influence power converter dynamics [16]. PVG dynamic resistance is highly dependent on the operating point and may experience

major variation. Hence, the combined PVG-converter-load system dynamics undergo significant changes.

These deviations are caused by PVG operation point variations and directly influence the system, from damping up to losing stability [17].

Voltage control structures often utilize an inner current loop, which, in addition to inherent overcurrent protection, enhances robustness and reduces system order, simplifying the controller design. PVG is typically integrated with a boost power stage. In this paper, this is accomplished by sweeping into the "buck" convertor, which leads to the cascaded control shown in Fig. 1.

The controllers are usually decoupled in terms of bandwidth to allow proper independence of loops and ease of control design.

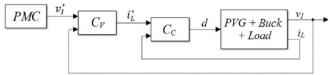


Fig. 1. Cascaded control structure under investigation.

As shown in [18], [19], the voltage loop small-signal dynamics must be contingent on PVG as well as on load properties. Nevertheless, since load properties in PVG-based systems are slow-varying, the source influence is far more significant. Conventionally, PVGs were terminated by large capacitors and represented by ideal current sources in control design development. In this paper, A novel mathematical analysis approach for PVG-Buck-Load transfer function is presented. The new method employs variables that allows rewrite the system transfer function and simplify it. The new terms enable a bridge system transfer function into a form that assist to set the nominal plant without entering to an unstable zone due the existence of the right hand pole in buck converter. In addition, the new method allows us to reveal the system behaviour from main components perspective and enable us to present control performance analysis under a range of conditions and constrains. To verify the new method a deep analysis is made, then setting the system behaviour by preferring controller variables and then affirming the proposed theory by simulation.

II. PROBLEM FORMULATION

The system under consideration is shown in Fig. 2, in which the PVG capacitor is interfaced with buck power stage (BuPS). The converter is integrated to a battery (in the case of autonomous systems) or coupled to the grid by a voltage control inverter. In both methods, the converter load is of a voltage-source type and may commonly be represented by a Thevenin equivalent circuit with slow-varying parameters.

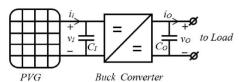


Fig. 2. Buck converter interfaced PVG.

The resultant average model of the combined PVG-BuPS-Load system is shown in Fig. 3. L and d represent converter inductance and duty cycle, respectively, R_B and V_B denote load Thevenin parameters, and the PVG is built from current source and a paralell resistor as a Norton equivalent circuits in the electrical diagram of solar cell, when R_{PV} reflects PV static resistance at MPP.

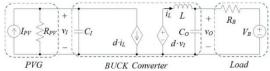


Fig. 3. Average model of combined PVG-Buck Converter-Load system.

The combined system is governed by the following set of equations, assuming a continuous conduction mode:

$$\begin{cases} C_{I}\dot{\tilde{v}}_{I} = -R_{PV}^{-1}\tilde{v}_{I} - I_{L}\tilde{d} - D\tilde{i}_{L}, \\ L\dot{\tilde{i}}_{L} = D\tilde{v}_{I} + V_{I}d - \tilde{v}_{O}, \\ C_{O}\dot{\tilde{v}}_{O} = \tilde{i}_{L} - R_{BAT}^{-1}\tilde{v}_{O}, \end{cases}$$
(1)

linearized around an operation point given by:

$$\begin{cases} \frac{P_{PV}}{V_I} = I_{PV} - V_I R_{PV}^{-1} = DI_L, \\ DV_I = V_O, \\ \frac{P_{PV}}{V_O} = I_L = (V_O - V_{BAT}) R_{BAT}^{-1}. \end{cases}$$
 (2)

As internal battery resistance (R_{BAT}) is small-scale in size, zero value can be assumed. And as V_0 can be declared constant due to battery characteristics, this leads to the assumption that the small signal of V_O has no effect on internal voltage [20]. Consequently, the next set of equations is as follows:

$$\begin{cases} C_I \dot{\tilde{v}}_I = -R_{PV}^{-1} \tilde{v}_I - I_L \tilde{d} - D\tilde{i}_L, \\ L \dot{\tilde{i}}_L = D\tilde{v}_I + V_I d. \end{cases}$$
 (3)

Taking into account the cascaded control structure of Fig. 1 and with the conditions that the current loop is properly closed and its bandwidth is considerably higher than the voltage loop bandwidth, perfect current tracking may be presumed

$$\tilde{i}_L(t) = \tilde{i}_L^*(t). \tag{4}$$

Substitution of (4) into (3) implements Laplace transform and rearranges the results, yielding

$$\frac{\tilde{v}_{i}}{\tilde{i}_{L}^{*}}(s) = -\frac{D + \frac{I_{L}}{V_{I}}Ls}{C_{i}s + R_{PV}^{-1} - R_{s}^{-1}},$$
(5)

where

$$R_s^{-1} = \frac{I_L}{V_I} D. {(6)}$$

Due to the influence of R_{PV} and R_S on the transfer function (5) and throughout the change of PV voltage values, an unstable situation can occur. It is expressed when the system operates lower than MPP on the PV curve, as in Fig. 4. Thus, caution in design is necessary, in case the load is a Li-ion battery with minimum and maximum voltages that must be constrained within nominal operating values. Therefore, varying regions of PV operated voltage may be compelled, leading to different responses. Consequently, the system response is no longer as designed. In practice, it can change from highly overdamped to under-damped up to system instability and oscillation. To overcome the complication of controlling the full range of operation, a worst-case design is necessary. Accordingly, a reshaping transfer function is required.

It is noteworthy that output voltage should be restricted to a certain maximum value when the load is a battery or stand-alone system. As a result, this condition directly influences the input voltage, and it is important to note that input voltage dynamics are highly dependent on both environmental conditions and the operating point. Traditionally, designers ignored these influences by using a large capacitor. However, when using an autonomous system such as a lithium battery as a load, the internal dynamics highly influence the performance of the input voltage. Consider a representative nominal I-V curve of a PVG for certain values of irradiation G_0 and temperature $T_{\rm O}$, as shown in Fig. 4. Note that PVG behaves as a current source at voltages lower than MPP, and as a voltage source at voltages higher than MPP. That is, it maintains high dynamic resistance in the former case and low dynamic resistance in the latter. In order to compensate for the input voltage loop, the PI controller should be of the form

$$C(s) = -\frac{\omega_c}{s} (P_N^{-1}). \tag{7}$$

As depicted in [1] where the boost converter is regulated, it cannot be employed. Thus ω_c is the preferred crossover

frequency, in order to create the "ideal" loop gain ω_c/s . However, attaining "ideal" loop gain is a nontrivial task, since R_{PV} and R_S vary considerably and directly impact the denominator of the transfer function (5). Due to significant change in R_S value, a positive pole can result in an unstable system (8)

$$(C_i s + R_{PV}^{-1} - R_s^{-1}) \Rightarrow (R_{PV}^{-1} - R_s^{-1}) < 0.$$
 (8)

In order to solve this problem, a PI controller should be designed for the worst-case scenario $\left(v_{pv}^{\min}\right)$ as determined in Fig. (4).

Analysing regions of operation and determining the v_{pv}^{min} or v_{pv}^{max} (best-case) is required. Thus, some manipulations in (5) are necessary as the goal is to separate the gains to determine denominator influence on stability. Rearranging (5) obtains the transfer function (9). As a result, two separate parts are obtained: gain (G) and system.

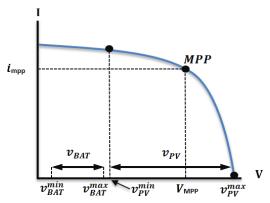


Fig. 4. v_{pv} operation area.

$$P_{(s)} = \frac{\tilde{v}_i}{\tilde{i}_L^*} = -\frac{D(1 + \frac{LI_L}{V_I D}s)}{(R_{PV}^{-1} - R_s^{-1})(1 + \frac{C_i}{R_{PV}^{-1} - R_s^{-1}}s)}.$$
 (9)

Hsere the gain is

$$G = \frac{D}{(R_{DV}^{-1} - R_c^{-1})}. (10)$$

And for the remainder of the equation, a few assumptions are made. As lower frequencies were $\omega \gg \omega_o$ the numerator approaches one

$$1 + \frac{LI_L}{V_I D} s \rightarrow \approx 1. \tag{11}$$

And for more simplicity, ω_{pv} is defined as

$$\omega_{pv} = \frac{R_{PV}^{-1} - R_s^{-1}}{C_i},\tag{12}$$

eventually resulting in

$$P_{(r)} = \frac{\tilde{v}_i}{\tilde{i}_L^*} = \approx -\frac{G}{1 + \frac{s}{\omega_{pv}}}.$$
 (13)

The next design stage is the determination of v_{pv}^{\min} or v_{BAT}^{\max} limitations. Since the load is a Li-ion battery, therefore, the voltage is limited to a maximum permissible. Consequently, determined as v_{BAT}^{\max} , and respectively to Fig. 4, leading to v_{pv}^{\min} as the worst-case scenario:

$$\begin{cases} Wc: \frac{v_{BAT}^{\min}}{v_{\min}} < D_{wc} < \frac{v_{BAT}^{\max}}{v_{\min}}, \\ R_{pv} > R_s \Rightarrow R_{PV}^{-1} < R_s^{-1}, R_s = \frac{v_{\min}}{I_{sc}}, \\ when_{R_{pv}} \to \infty \Rightarrow \omega_{pv_{wc}} = -\frac{1}{R_s C_i}. \end{cases}$$
(14)

The worst-case $\left(v_{pv}^{\min}\right)$ scenario is when the PV operates at the left side of the i-v curve, as in Fig. 4, where the duty cycle is limited between v_{BAT}^{\min} to v_{BAT}^{\max} with respect to v_{pv} . Accordingly, ω_{pv_wc} is dependent on the R_s value (14). It should be noted that the minus sign in (14) can in uncorrected design easily lead to instability.

On the other hand, when PV voltage is approximately MPP or higher, the system from the control perspective is functioning in the best-case zone. Hence, v_{pv} is bonded between min and max PV operation voltages. That is, it is determined and limited by the upper threshold battery voltage due to internal protection of the battery or a safety reason (v_{pv}^{\min} scenario). The duty cycle (D) in the buck convertor is the ratio between v_{out} to v_{in} (15). Therefore, v_{\max} should be v_{oc} (PV open circuit voltage) as indicated by best-case (Bc) voltage (v_{pv}^{\max}). It should be emphasized that the result in (15) means ω_{pv} will cause a slower response time:

$$\begin{vmatrix} v_{BAT}^{\min} < v_{o} < v_{BAT}^{\max}, \\ Bc : D = \frac{v_{o}}{v_{I}}, \frac{v_{BAT}^{\min}}{v_{oc}} < D_{Bc} < \frac{v_{BAT}^{\max}}{v_{oc}}, \\ R_{pv} < R_{s} \Rightarrow R_{PV}^{-1} > R_{s}^{-1}, \\ when_{R_{s}} \rightarrow \infty \Rightarrow \omega_{pv_Bc} = \frac{1}{R_{PV}C_{i}}. \end{aligned}$$
(15)

A typical approach to compensation (13) utilizes the PI controller, employing nominal values of capacitance (generally producer declared) and PVG dynamic resistance, typically calculated at standard test condition (STC) MPP [21]

$$C_{wc}(s) = -\frac{k_{p_{-}wc}s + k_{i_{-}wc}}{s}.$$
 (16)

The resulting closed-loop transfer function is dependent on two different conditions, Bc and worst-case (Wc). Analysing the system from the Wc scenario produces a different group of equations

$$P_{wc} = -\frac{|G_{wc}|}{1 - \frac{s}{\omega_{pv}} \frac{1}{w_{c}}}.$$
 (17)

 P_{wc} (17) represents the plant when using the Wc position for designing the controller:

$$cl_{wc} = \frac{C_{wc} P_{wc}}{1 + C_{wc} P_{wc}},$$
(18)

$$= \frac{\omega_{pv_wc}G_{wc}k_{i_wc}(1 + k_{p_wc}/k_{i_wc})}{s^2 + s(G_{wc}k_{p_wc} - 1)\omega_{pv_wc} + G_{wc}k_{i_wc}\omega_{pv_wc}}.$$
 (19)

Equation (19) establishes the closed-loop (cl) Wc system, and uses the Wc scenario for development and calculation of controller constants, creating a stable system in all areas of operation. Utilizing (19) and shaping it as a standard form of an underdamped system allows extracting natural frequency and damping ratio. Thus, (20), (21) allows extracting Wc values of natural frequency (ω) and damping ratio (ξ) , respectively, for any given set of variables, and will be used for further investigation:

$$\omega_{wc} = \sqrt{G_{wc}k_{i_wc}\omega_{pv_wc}}, \qquad (20)$$

$$\xi_{wc} = \frac{(G_{wc}k_{p_wc} - 1)\omega_{pv_wc}}{2\omega_{wc}}.$$
 (21)

It should be noted that constants k_p and k_i are calculated to Wc. Analysis of Bc will demonstrate the influence of Wc on controller design.

Evaluating the Bc scenario generates the next group of equations

$$P_{Bc} = -\frac{G_{Bc}}{1 + \frac{s}{\omega_{pv_Bc}}},$$
 (22)

with PBc (22) representing the plant at Bc operation:

$$cl = \frac{C_{wc} P_{Bc}}{1 + C_{wc} P_{Bc}}. (23)$$

This leads to the close-loop (23) while using a Wc controller design

$$cl_{BC} = \frac{\omega_{pv_Bc}G_{Bc}k_{i_wc}}{s^2 + s(G_{Bc}k_{p_wc} + 1)\omega_{pv_Bc} + G_{Bc}k_{i_wc}\omega_{pv_Bc}}.$$
 (24)

Eventually, the closed-loop receives the Bc component in the form of (24):

$$\omega_{Bc} = \sqrt{G_{Bc} k_{i_wc} \omega_{pv_Bc}}, \qquad (25)$$

$$\xi_{Bc} = \frac{(G_{Bc}k_{p_wc} - 1)\omega_{pv_Bc}}{2\omega_{Bc}}.$$
 (26)

Equations (25) and (26) demonstrate the influence of the Bc operation point, whereas the system uses a controller designed for Wc. The effects of k_p and k_i constants are clearly shown in the rate of response and restraint of the system. The most noticeable variable between Wc and Bc is the apparent value of ξ , due to significant gain magnitude and ω_{pv} variation. Consequently, this leads to different ξ values as shown in Fig. 8, while the second variables are G and ω_{pv} as demonstrated in Fig. 6 and 7, respectively. ω_{pv} and G possess significant changes and due to depending on k_{i_wc} values, eventually force the system to behave not as required.

III. BEHAVIOURAL ANALYSIS

A design for Wc due to $v_{bat}^{\rm max}$, indicates that while there is no steady state error for step input, transient performance would be heavily influenced by environmental variables and the operating point. For a verification of the receiving transfer function Pr (13) which does not include all the components of the plant such as Vo and load (v_{BAT} , R_{BAT}), see Fig. 5. Using PV values of Table I demonstrates how the Bode curve is performed when using a complete transfer function, including all the components of small signal analysis (blue line). On the other hand, the red line representing the simplified transfer function, does not include all components. Analysis of the Bode diagram reveals that at working frequency, the lines are correlated. Consequently, the system will respond as designed when using reduced transfer function.

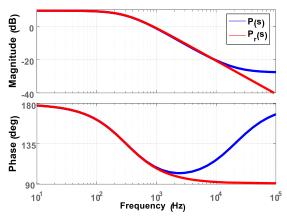


Fig. 5. Bode response of complete and simplified (Pr) plants.

In order to demonstrate the behaviour of the components that significantly influence the control response, a linearization of the PV i-v curve is used. The curves are separated into two main sections: 1) the constant current section and 2) the constant voltage section [22]. Each part is

linearized for simplicity but still provides a good result in imitation of a real i-v curve.

With a focus on ω_{pv} , G, ξ , $R_{PV}-R_s$, in accordance with a Wc control design scenario, which is designed for stability in all conditions, the controller is forced to respond to a variety of working points, while the system is mostly nonfunctional at Wc. Consequently, at a different operating point, the influence of the coefficient in the controller equation (19) on the closed-loop response is incredibly significant. Simulations in Fig. 6–Fig. 9 demonstrate variation in values along the i-v curve.

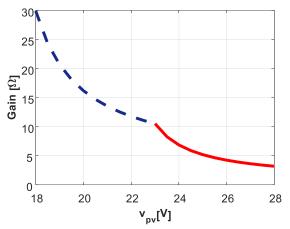


Fig. 6. Gain versus v_{pv} .

Figure 6 reveals variation in gain (10) across the entire PV voltages, which is stirred from almost zero to significant values. As the blue line represents the constant current section and the red line represents the constant voltage section, it is important to note that, crossing point between blue line and red line presenting the MPP point. Hence, it clearly shows the difference in gain behaviour and magnitude before and after the MPP point.

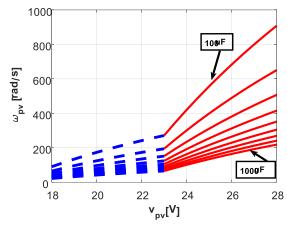


Fig. 7. ω_{pv} versus v_{pv} .

Figure 7 reveals the significant variation of ω_{pv} (12). In this case, the internal capacitor has an important influence, actualized by each curve, which possesses a different capacitance value and changes from 1000 uF to 100 uF. It is clearly shown that as the voltage shifts from MPP where $R_{PV}-R_s=0$, ω_{pv} increases sharply, influencing the system open-loop response. The effect becomes significant as the capacitor value decreases.

The damping ratio (ξ) plays an important role from the control perspective, predicting performance along the operation points, and, additionally, for different internal capacitors, as shown in Fig. 8. Thus, performance can be assessed in advance and the correct component for the system can be pre-selected. Figure 8 shows the damping ratio increase as voltage increases, meaning that at some point the system will not be fast enough for proper tracking.

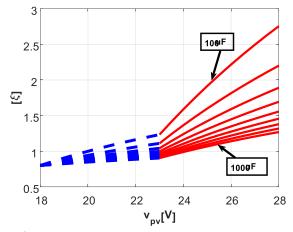


Fig. 8. ξ versus v_{pv} .

Since the controller coefficients k_p and k_i are calculated for Wc, Thus ,the rest of the components in the close-loop transfer function $\left(\omega_{pv}G\xi R_{PV}-R_s\right)$ must be modified and adapted to the new condition due to dependence on $R_{PV}-R_s$. Figure 9 reveals the significant change of $R_{PV}-R_s$ along the operation voltage, the vertical black line representing the chosen voltage for worst-case scenario. It is thus clearly shown that choosing the incorrect voltage can lead to an unstable system.

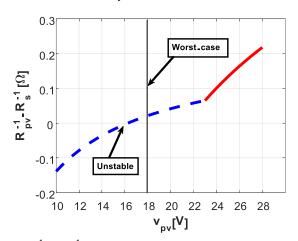


Fig. 9. $R_{PV}^{-1} - R_s^{-1}$ versus v_{pv} .

Extended system performance evaluation of the control system is demonstrated in Fig. 7, Fig. 8, where the internal capacitor is changed from 1000 uF to 100 uF. The variability value influences ω_{pv} and ξ . During the time that the capacitor values decreased, ω_{pv} accelerates in Fig. 7 and ξ is increases in Fig. 8. Additionally, each curve which represents a capacitor value also behaves similarly

according to the voltage value of maximum power point (MPP). Hence, as the voltage value recedes from the MPP to the current source aspect or voltage source aspect its influence is the same. That is, ω_{pv} grows faster and ξ increases as the voltage changes from Wc to Bc.

IV. SIMULATIONS

To demonstrate the revealed performance, take into account a 100 µF capacitor-terminated BPS-interfaced PVG with STC parameters summarized in Table I. Consider PVG dynamic resistance bounded by $\left(i_{sc} / v_{min}\right)~0.32~\Omega \leq R_{PV} < 10~\Omega~\left(i_{min} / v_{max}\right)$. That is, i.e. it may experience almost an exponential increase when the PVG operating point varies from maximum to minimum operation voltage.

TABI.	ΕI.	PV	STC	DATA	١.
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Open circuit voltage	28 V
Short circuit current	5.9 A
Maximum power voltage	23 V
Maximum power current	5.1 A
Rated power	117 W

Moreover, assume that input capacitance value may reside in the range of 1000 μ F > CI > 100 μ F. Consequently, all possible ω_{nv} and ξ are already shown in Fig. 7, Fig. 8. Figure 10 demonstrates tracking simulation results of 1 sun irradiation, operating a PVG voltage loop compensated by a calculating controller (16) for a Wc scenario, with $\omega_c = 200\pi$ rad·s⁻¹, $c_{in} = 100 \mu F$ and $R_{min} = 0.32 \Omega$ (STC value). The dashed line indicates system performance when a 50 uF internal capacitor is used. Closed-loop dynamics dependence on the operating point is quite evident. The system is highly over-damped at voltages higher than MPP (high ξ value) and under-damped for voltages lower than MPP (low ξ value). The dashed line indicates the use of a restricted capacitor smaller than the minimum required. By combining Fig. 6-Fig. 9 G, ω_{pv} , ξ , $R_{PV}-R_{s}$ it is possible to establish the result of the dashed line during the planning stage and can help regulate the correct capacitor for required performance.

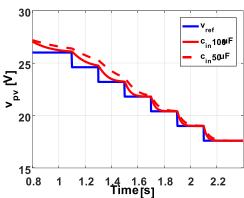


Fig. 10. System response for different internal capacitor.

V. CONCLUSIONS

This paper presented a novel mathematical analysis approach for photovoltaic generator. Control strategy of

current mode control interfacing a buck DC-DC converter and PVG transfer function as a unified system. Since PVG is characterized by several operation point and dependent environmental-variable nonlinear behaviour the control task nontrivial. The proposed method rewrites the system transfer function and simplified it and the new mathematical equations of unified system plant bounded by worst case scenario are revealed. Hence, the simplified transfer function assists to set the nominal plant without entering an unstable region due the existence of the right-hand pole in buck converter. Moreover, the revealed system behaviour from main components perspective enables the user to preestimate system behaviour for specific conditions or regions of operation, aimed to optimal control design. The new analysis method is essayed by series of simulations that validate the proposed strategy.

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