Parallel and Pipelined Hardware Implementation of Radar Signal Processing for an FMCW Multi-channel Radar

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EVALUATION

Scientific quality

Relevance in the field

Originality

Language and clarity of presentation

RECOMMENDATION

DECLINE SUBMISSION

ACCEPT (AS IT IS)

ACCEPT (AFTER MINOR REVISIONS)

ACCEPT (AFTER MAJOR REVISIONS)

I WANT TO SEE THE PAPER AFTER REVISIONS

REMARKS (please, use an additional page if necessary)

The manuscript presents a method to implement an algorithm to detect the range and velocity of a target on FMCW radar. The main contribution of the manuscript is to propose fine-grained DBF architecture and implement it. But description about the previous works was not sufficient. For completeness and better understanding of the paper, please add a separate section to describe the structure, operation, strong and weak points of the previous works.

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6 Nov 2014